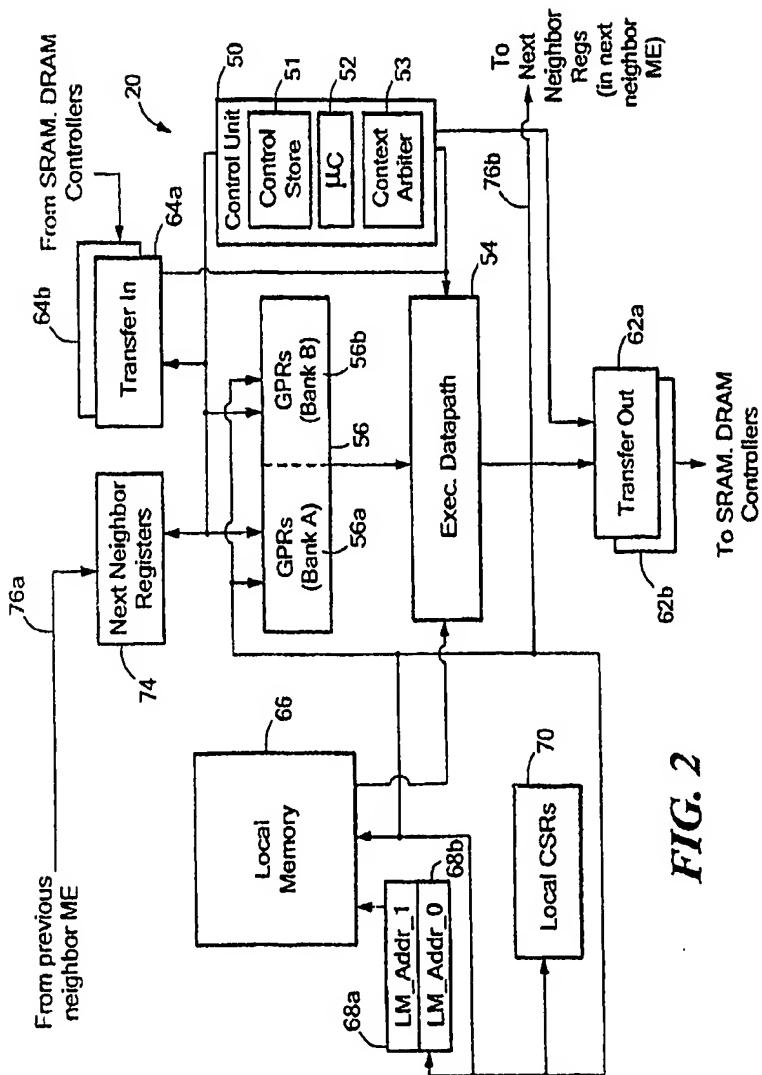




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**FIG. 2**

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PC	Src1	Src2	Dest	I/O Xfer	Xfer Count	I/O Direction
...						

182    184    186    188    190    192    194

122

180

⋮

**FIG. 9**

Time/Cycle	Thread State	Thread Number	PC
0	Executing	0	0
1	Executing	0	1
2	Aborted	0	2
3	Executing	1	0
4	Executing	1	1
5	Aborted	1	2
...			

202    206    204    208

134

**FIG. 10**